



PATENT
Docket No. 29250-000149/US

#7
5/4/02
J.S.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Alex MATUSEVICH Conf: 1477
Application No.: 09/156,761 Group: 2663
Filed: September 18, 1998 Examiner: Jasper C. KWOH
For: TDMA COMMUNICATION SYSTEM AND METHOD INCLUDING
DISCONTINUOUS MODULATION FOR REDUCING ADJACENT AND
CO-CHANNEL INTERFERENCE

DRAWING CORRECTION APPROVAL REQUEST

Assistant Commissioner for Patents
Washington, D.C. 20231

RECEIVED

May 1, 2002

MAY 0 6 2002

Dear Sir:

Technology Center 2600

Applicant respectfully requests the Examiner's approval of the following drawing correction indicated in red ink on the attached sheets. The following is the correction.

Figures 1-3 have been designated by a legend of --Prior Art--.

No new matter has been entered via the drawing correction. Corrected formal drawings will be prepared in accordance with the Notice of Draftperson's Patent Drawing Review Form PTO-948 and will be filed upon approval by the Examiner and subsequent indication of allowance of the present application.

If there are any outstanding matters remaining in this application, the Examiner is invited to contact Timothy R. Wyckoff, Reg. No. 46,175, at the number listed below in the Washington, D.C. area in order to discuss these matters.

If necessary, the Commissioner is hereby authorized in this, concurrent, and further replies, to charge payment or credit any overpayment to Deposit Account No. **08-0750** for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

HARNESS, DICKY & PIERCE, P.L.C

By:  #46,175
Gary D. Yacura, Reg. No. 35,416

GDY/TRW:tljw

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Attachment: As stated above

FIG. 1 (Prior Art)

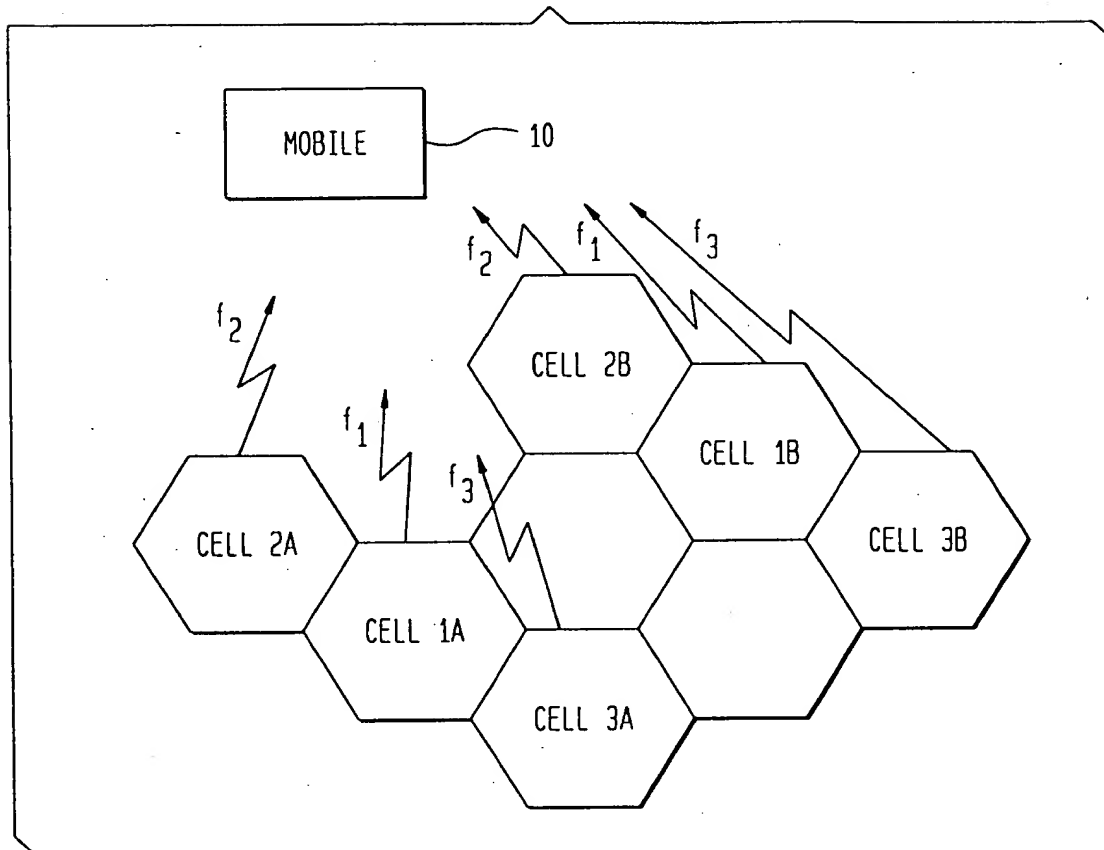
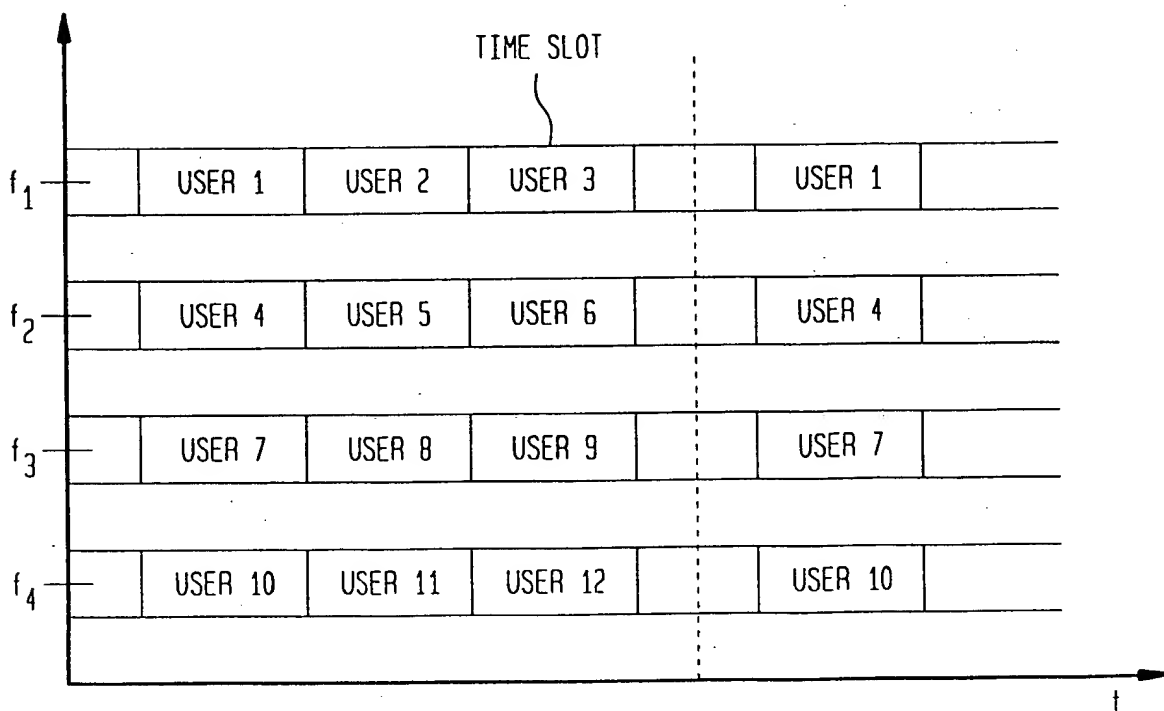


FIG. 2 (Prior Art)



approved
g/k
7/5/02

Timing diagram for a 20 msec frame. The frame is divided into nine segments: SYNC1, CTRL + DATA, CDL, SYNC2, CTRL + DATA, CDL, SYNC3, CTRL + DATA, and CDL. A bracket below the first three segments (SYNC1, CTRL + DATA, CDL) indicates a duration of 6.67 msec.

Approved
7/5/02

TIME SLOT

30 KHz

t